



XAVIER PAUL RAJAN H
GUEST FACULTY

Contact

Address : DDU KAUSHAL KENDRA
Bharathidasan University, Khajamalai campus
Tiruchirappalli - 620 024
Tamil Nadu, INDIA

Employee Number :
Date of Birth : 14-07-1982
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Academic Qualifications: M.A./M.Sc./M.Phil./Ph.D./

- Master of Engineering (M.E.) in **Communication Systems** from **BIT Campus – Anna University Chennai (Trichy Campus)** with CGPA of 7.
- Bachelor of Engineering (B.E.) in **Electronics and Communication Engineering** from Adhiyamaan college of Engineering, Hosur affiliated to **Periyar University, Salem** with **74.7%**.

Teaching Experience: 11.5 Years

| Sl. No. | Name of the Company/Institution | Position held | Duration | | Responsibility |
|---------|--|----------------------|------------|-----------|--|
| | | | From | To | |
| 1 | DDU KAUSHAL Kendra Bharathidasan University | Guest Faculty | July 2016 | Till date | University Examination-Incharge internship Incharge |
| 2 | Pavendar Bharathidasan Institute of Information Technology – Trichy | Assistant Professor | June 2011 | June 2016 | Class Co-ordinator University Examination Incharge |
| 3. | Slash Support India Pvt Ltd - Chennai | Application Engineer | Jan 2006 – | Mar 2008 | Configuring and Trouble Shooting of Networking based Products VOIP Routers (Wired and Wireless). |

Research Experience: 2 Years

Additional Responsibilities

1. Handling the responsibilities of MOU with industries for Internship programmes for Troubleshooting and maintenance of Electrical and Electronics Equipment.
2. Incharge for University examinations
3. **Exam Cell Co-ordinator** for the Pavendar Bharathidasan Institute of Information Technology to conduct Anna University Examinations for the 6 Semesters
4. Class co-ordinator for the II year ECE students in Pavendar Bharathidasan Institute of Information Technology.

Areas of Research

Digital Image Processing, Embedded systems

Research Supervision / Guidance

| Research Project | Program of Study | Completed | Ongoing |
|-------------------------|-------------------------|------------------|----------------|
| | Ph.D. | | |
| | M.Phil. | | |
| | PG | | |
| | UG / Others | 40 | |

Publications

| International | | National | | Others |
|----------------------|--------------------|-----------------|--------------------|--|
| Journals | Conferences | Journals | Conferences | Books / Chapters / Monographs / Manuals |
| | 1 | | | |

Events organized in leading roles

Number of Seminars / Conferences / Workshops / Events organized: 01

1. Organised a One day Workshop on Checking and Validation of Electronic Equipments in Pavendar Bharathidasan Institute of Information Technology

Events Participated (optional)

Conferences / Seminars / Workshops: 02

1. Participated a One Day Workshop on ARM Processor in Roever Engineering College – Perambalur
2. One day FDP on **Enhancing learning outcome through effective teaching** organized by Internal Quality Assurance Cell &UGC-HRDC, Bharathidasan university Tiruchirappalli, during 28th November 2018.
3. Paper presentation on an international conference of Innovating pedagogy 2019 in the title “ Vocational and Employability skills in Electrical and Electronics maintenance through internship training activities among the students of Engineering on 17-19 August 2019.

Other Training Programs

1. Certified with **Post Diploma in Internet Technologies** from “TREC–STEP”, in National Institute of Technology, Trichy.
2. Certified with **Masters Diploma in OrCad** from CADD centre, Trichy.